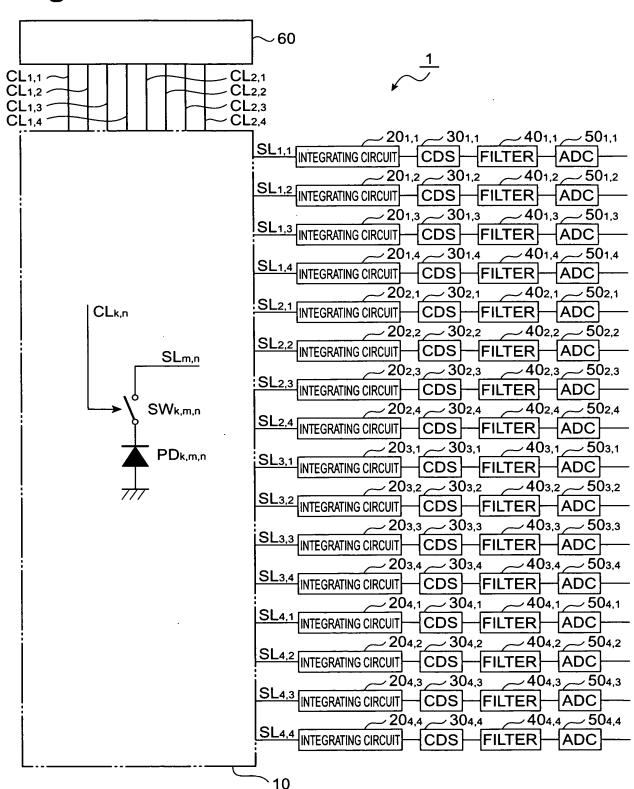
1/4

Fig.1



2/4 SL243 SL242 SL1,1 SL1,2 SL13 - SL14 SS 32 SS 33 SS 33 SS 33 SS 33 SS 34 SL4.3 SL4.3 SL4.3 S₂₂2 SW2,2,4 SW2,4,4 SW2,1,4 SW2,3,4 **CL2,4** PD2,1,4 PD2,2,4 PD2,3,4 PD_{2,4,4} SW2,1,3 SW2,2,3 SW2,3,3 SW2,4,3 \sim CL23 PD2,1,3 PD2,2,3 PD2,3,3 PD2,4,3 ·\SW2,3,2† SW2,1,2 \SW2,2,2 SW2,4,2 l PD2,1,2 \sim CL2,2 PD_{2,2,2} PD2,3,2 PD_{2,4,2} SW2,1,1 \SW2,2,1 \ SW2,3,1 SW2,4,1 PD2,1,1 PD2,2,1 PD2,3,1 CL2.1 PD_{2,4,1} SW1,1,4 \SW1,4,4 ; SW1,3,4 SW1,2,4 PD_{1,1,4} PD_{1,3,4} PD_{1,4,4} PD_{1,2,4} SW1,2,3 SW1,3,3 SW1,4,3 L SW1,1,3 CL1,3 PD_{1,1,3} PD_{1,3,3} PD_{1,2,3} PD_{1,4,3} SW1,1,2 SW1.2.2 SW1,3,2 SW1,4,2 L \sim CL1,2 PD_{1,1,2} PD_{1,3,2} PD_{1,4,2} PD_{1,2,2} ►\°SW1,4,1 ^L \SW1,3,1 SW1,2,1 SW1,1,1 \CL1.1 PD_{1,1,1} PD_{1,2,1} PD1,3,1 PD_{1,4,1}

Fig.2

3/4 ADC A₃₀ C_{32} ر د ک A_{20} SL_{m,n} $SW_{k,m,n}$ PD_{k,m,n}

Fig.3

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